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In re Applicant: Subhasish Mitra et al. § Art Unit: 2138
Serial No.: 10/656,013 § Examiner: Phung M. Chung
Filed: September 5, 2003 § Docket: ITL.0971US
For: Compacting Circuit § P16171
Responses § Assignee: Intel Corporation
§

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed March 23, 2007, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: April 25, 2007
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 713/273-9300) on the date indicated above.

Cynthia L Hayden
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